



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Van Ginneken, et al. ]  
Appl. No. 09/300,540 ]  
Filed: April 27, 1999 ]  
Title: Method for Storing Multiple Levels ]  
of Design Data in a Common Database ]

Examiner: Phallaka Kik

Art Unit: 2825

October 9, 2001

Commissioner for Patents  
Washington, D.C. 20231

**AMENDMENT**

Sir:

Responsive to the Office Action dated June 6, 2001, entry of the following amendments and remarks are respectfully requested.

IN THE CLAIMS: ✓

Please cancel claim 22.

Please amend the following claims:

21. (amended) A computer-readable medium storing a data structure for representing digital

10/29/2001 VVANHALL 00000004 033975 09300540  
circuits, wherein:

01 FC:102  
02 FC:103  
03 FC:115

84.00 CH

288.00 CH The data structure includes a root containing at least one library;

110.00 CH

each library contains at least one entity;

each entity contains at least one model and at least one port;

each model contains at least one cell, at least one net and at least one model pin;

each cell contains at least one cell pin; and

wherein the data structure is used to create a data model representing a digital circuit, which data model includes the at least one cell pin, the at least one cell, the at least one net, the at least one model pin and the at least one port, which are obtained by selecting the at least one library, the at least one entity, and the at least one model, and wherein the data model thus created includes both logical synthesis information and physical placement information.

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